

## DSG-NPS R&D Meeting Minutes

**Date: November 22, 2022**

**Time: 02:00PM – 03:00PM**

*Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran*

### 1. Hardware fabrication

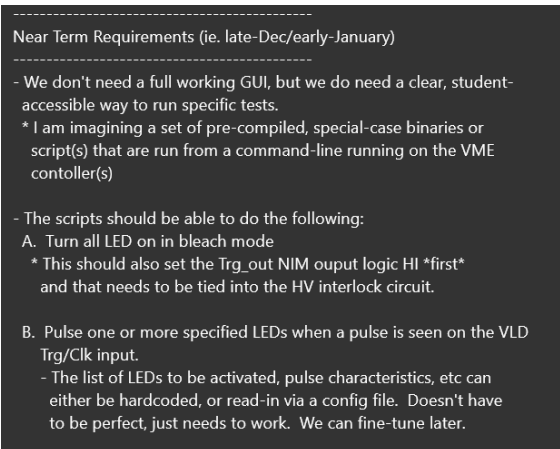
*Aaron Brown, Mindy Leffel, and Marc McMullen*

1. Aaron is generating an Excel spreadsheet detailing the pinout for the 50-pin, D-sub, extension cables
  - Need 12 cables — 10 cables for the five Keysight terminal blocks and two spares (each terminal block requires two cables)
  - 60 ft. cable run (per Paulo Medeiros); only 500 ft. on hand
  - Contacted Showmecables.com to find out lead time for additional 500 ft. of cable; waiting for response

### 2. VLD GUI

*DSG*

1. With regards to the “Near Term Requirements”, Brian will begin working on a GUI that utilizes the existing Linux drivers; estimated time of completion is January 2023



Screenshot taken from email from Brad Sawatzky received 11/21/2022

### 3. Thermal readback

*Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, and Tyler Lemon*

1. Debugging complete; Pablo will retest all Phoebus screens on *cdaq13* using *cdsg* account

### 4. Low voltage controls

*Mary Ann Antonioli and Tyler Lemon*

1. Tyler is developing an IOC that will communicate with the MPOD
  - Attempting to run IOC on *cdaq13*; experienced issues during initial testing with setting up the SNMP command module for the IOC
  - Tyler will contact Brad Sawatzky to troubleshoot
2. Mary Ann is developing the EPICS Phoebus control and monitoring screen
  - Columns will be added to provide difference between set and readback values for current and voltage